

Diagram illustrating a TDMA frame structure. The frame is divided into slots labeled SLOT 1, SLOT 2, SLOT 3, SLOT 4, SLOT 5, SLOT 6, SLOT 7, SLOT 8, ..., SLOT N, Below the slots, a trapezoidal shape indicates the time slots for different channels: BCCH (Broadcast Control Channel) under SLOT 1, PCCH (Paging Control Channel) under SLOT 2, ARFCN (Arbitrary Frequency Channel Number) under SLOT 3, and ... under SLOT 4. A double-headed arrow at the bottom indicates the duration of the frame, labeled SUPERFRAME.

(Prior Art)

The diagram illustrates the structure of a TDMA frame and its placement within a superframe. At the top, a **TDMA FRAME (40 MS)** is shown, consisting of two **BLOCK (20 ms)** segments. Below this, a table of **SLOT (6.67 MS)** is provided, with slots numbered 1 through 6. Handwritten annotations include *202* pointing to the slot table, *200* pointing to the TDMA frame, *204* and *208* pointing to specific slots, *210* and *212* pointing to other slots, *214* and *216* pointing to the superframe, and *106* pointing to a boundary. Below the slot table, a sequence of 31 slots is shown, numbered 0 to 31. At the bottom, a **SUPERFRAME (640 MS)** is depicted, containing **FBCH**, **EBCH**, **SBCH**, **RESERVED**, and **SPACH** segments. A **BCCH** segment is also indicated below the first few slots.

Figure 2.

(Prior Art)



Figure 3

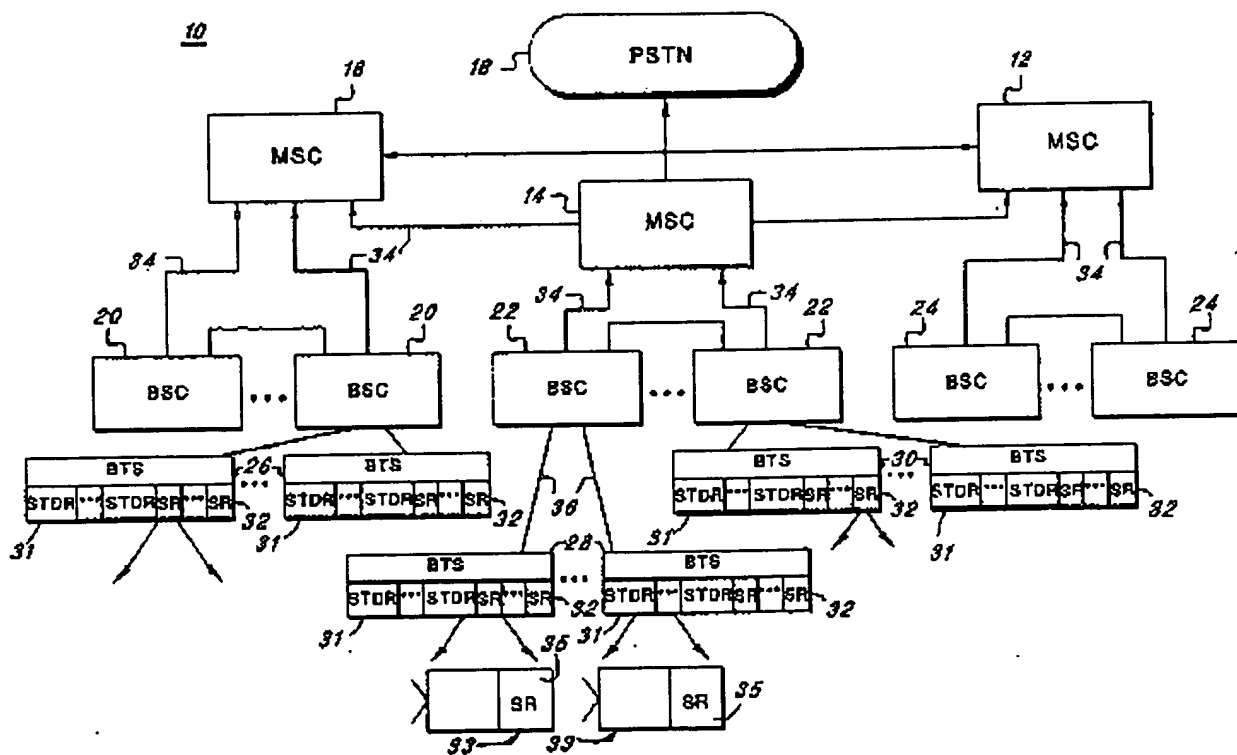


Figure 4

~302

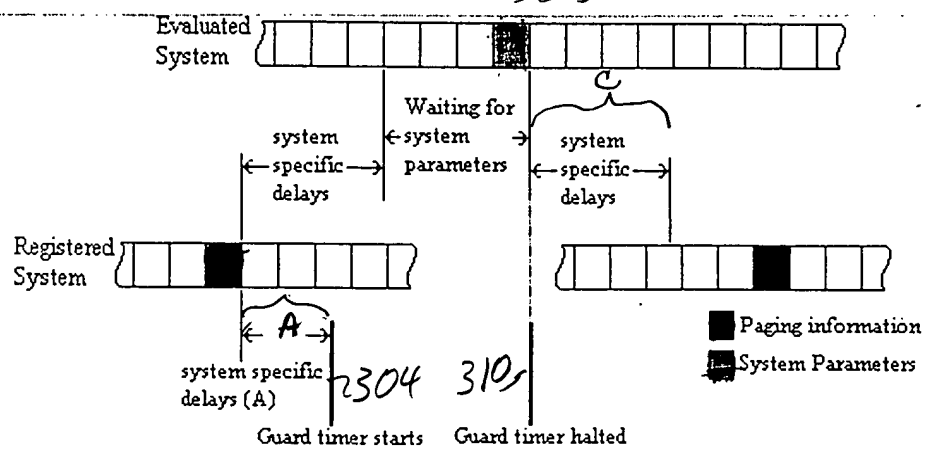


Figure 5

600

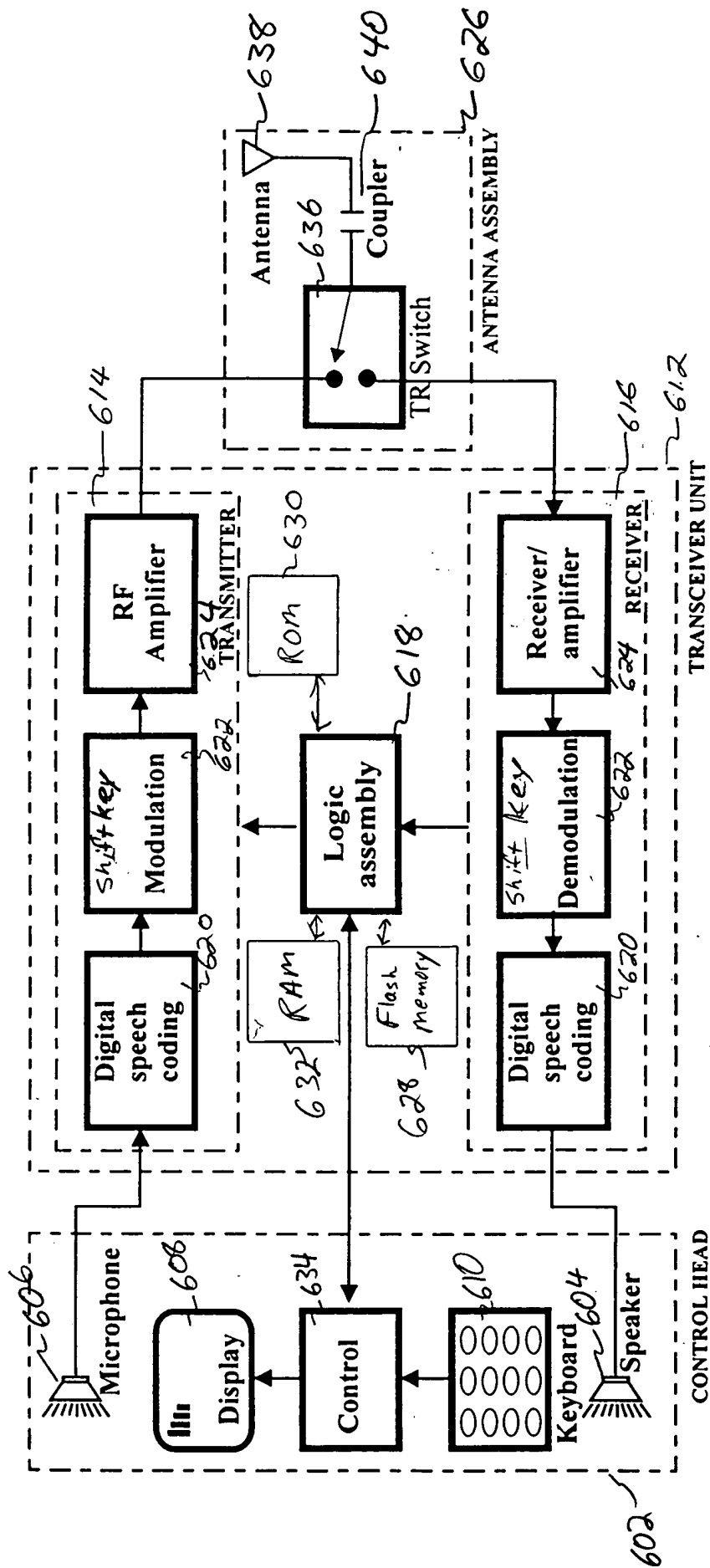


Figure 6